



6/IDS 2811
E. J. Jellis
10-22-03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

APPLICANT: Cheisan J. Yue et al. CASE NO.: P01,0365 H0002261
SERIAL NO.: 10/040,395 GROUP ART UNIT: unknown
FILING DATE: January 7, 2002 CONFIRMATION NO.: unknown
INVENTION: VARACTOR WITH IMPROVED TUNING RANGE

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Commissioner for Patents
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SIR:

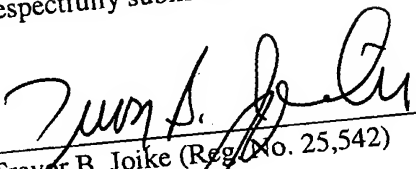
The patents and/or publications listed on the enclosed PTO Form-1449 are submitted pursuant to 37 CFR § § 1.56, 1.97(d), and 1.98. The undersigned hereby certifies pursuant to 37 CFR 1.97(d)(1) that each item of information contained in the Information Disclosure Statement was first cited in a communication from a foreign patent office in counterpart foreign application not more than three months prior to the filing of the Information Disclosure Statement. Copies of the patents or publications noted on the Form-1449 are enclosed herewith. Also, enclosed is a copy of the International Search Report.

The Commissioner is hereby authorized to charge any additional fees that may be required, or to credit any overpayment, to account No. 501519.

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An early and favorable action on the merits is respectfully requested.

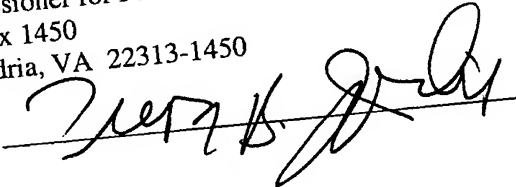
Respectfully submitted,


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CERTIFICATE OF MAILING

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| Form PTO-1449 | | | | | Docket No. P01,0365 | | Serial No. 10/040,395 | |
| INFORMATION DISCLOSURE CITATION IN AN APPLICATION (use several sheets if necessary) | | | | | Cheisan J. Yue et al. | | | |
| | | | | | Filing Date January 7, 2002 | | Group Art Unit unknown | |
| U.S. PATENT DOCUMENTS | | | | | | | | |
| Examiner's Initials | | Document Number | Date | Name | Class | Subclass | Filing Date If appropriate | |
| | AA | 5,965,912 | 10/12/99 | Stolfa et al. | | | | |
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| FOREIGN PATENT DOCUMENTS | | | | | | | | |
| | | Document Number | Date | Country | Class | Subclass | Translation | |
| | | | | | | | Yes | No |
| | AG | EP 1 024 538 A1 | 08/02/2000 | Europe | | | | |
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| | AQ | | | | | | | |
| OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.) | | | | | | | | |
| | AR | Mathi, "A SOI-CMOS Process for VLSI Technology", Navy Technical Disclosure Bulletin 10 (1984) Sept., No. 1, pgs. 117-121. | | | | | | |
| | AS | Svelto et al., "A Three Terminal Varactor for RFIC's in Standard CMOS Technology", IEEE Transactions on Electron Devices, Vol. 47, No. 4, April 2000, pgs. 893-895. | | | | | | |
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| Examiner | | | | Date Considered | | | | |
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